

Permanant Position: RF Engineer

Job Title:	RF Engineer
Job Location:	Cork, Ireland
Job Post Date:	1 May 2008
Salary:	€negotiable + pension + share options + IEEE Membership
Commencement Date:	Immediately

COMPANY BACKGROUND

Xerenet is a high-tech, product-focused start-up specializing in leading edge positioning technology. Our solutions encompass both infrastructure and infrastructure-less systems, for mobile, nomadic and fixed applications, based upon our world class expertise in: Analog/RF IC Design; Low Power Digital IC Design; Digital Communications and Signal Processing; Embedded Software and Systems; and Efficient Networking and Protocol Design.

Xerenet also offer ASIC design services and industrial training for engineers. Our design services offer turnkey solutions from concept to silicon. Our highly experienced team of design engineers and system architects are experts in digital communications and signal processing, digital design, layout and verification technologies.

The Xerenet training division offers courses in low power digital design, Advanced Verilog, DSP, MATLAB for systems engineering, and UNIX for ASIC engineers.

JOB DESCRIPTION

Xerenet are seeking a Radio Frequency Engineer to assist them in their goal to develop the technology for next generation positioning systems. Key to the demonstration of proof of concept of our ideas is the speedy roll-out of hardware prototypes and development platforms. We are therefore seeking an RF Engineer who can specify, design, layout and have delivered, discrete-component stuffed PCBs, with analog and digital capability, including for example: DSPs, micro-processors, FPGA's, PLLs, micro-controllers, AGC, LNA, antennas, matching networks, coupling units, super-heterodyne structures, analog filters, SAW filters, etc. In addition the engineer will need to have in-depth knowledge and experience of mixed signal RF IC design.

The job will initially entail responsibility in acquiring the correct hardware as specified in conjunction with the system architects. The RF Engineer will therefore be expected to work well in a team environment and to work closely with the system architects and digital designers. There will be responsibility for equipment purchasing, bill-of-materials costing and interaction with electronics suppliers. There is expected to be a significant amount of test and measurement work on the prototypes as they emerge. In the long term, the candidate will be required to lead in the porting of their designs to an mixed-signal ASIC.

The candidate may also be required to assist with the design/training services arm of Xerenet business, according to their skill-set.

JOB REQUIREMENTS

The candidate must:

- have > 10 years experience in the IC design industry
- have a Masters or PhD in the field of RF design
- have a working knowledge of MATLAB
- work well in a team environment
- be innovative and ambitious
- have experience with HSPICE, Verilog AMS
- have project management experience
- have interacted with customers

In addition it would be beneficial if the candidate had:

- antenna design experience
- worked on digital communications transceivers

Please forward your CV to the address below and we will acknowledge receipt within one working week:

email:	recruitment@xerenet.com	
web:	http://www.xerenet.com	
post:	Xerenet, The Rubicon Centre, Bishopstown, Cork, Ireland	