

Permanant Position: ASIC Design Engineer

Job Title:	ASIC Design Engineer
Job Location:	Cork, Ireland
Job Post Date:	1 May 2008
Salary:	€ negotiable + pension + share options + IEEE membership
Commencement Date:	Immediately

COMPANY BACKGROUND

Xerenet is a high-tech, product-focused start-up specializing in leading edge positioning technology. Our solutions encompass both infrastructure and infrastructure-less systems, for mobile, nomadic and fixed applications, based upon our world class expertise in: Analog/RF IC Design; Low Power Digital IC Design; Digital Communications and Signal Processing; Embedded Software and Systems; and Efficient Networking and Protocol Design.

Xerenet also offer ASIC design services and industrial training for engineers. Our design services offer turnkey solutions from concept to silicon. Our highly experienced team of design engineers and system architects are experts in digital communications and signal processing, digital design, layout and verification technologies.

The Xerenet training division offers courses in low power digital design, Advanced Verilog, DSP, MATLAB for systems engineering, and UNIX for ASIC engineers.

JOB DESCRIPTION

Xerenet are seeking experienced ASIC Design Engineers to work on-site at major European customer locations. You will be part of a customer design team working on some of the latest digital and analog ASIC designs.

The candidate will be highly motivated, ambitious, and flexible to turn their hand to tasks which may lie outside their core skill The candidate may also be required to assist with the Xerenet training business, according to their skill-set.

JOB REQUIREMENTS

The candidate must:

- have 5-10 years experience in an IC design environment
- have a Masters or PhD in electronics or related discipline
- work well in a team environment
- be innovative and ambitious
- have project management experience

In addition it would be beneficial if the candidate had:

- experience of hardware programming language (VHDL, Verilog, SystemVerilog, SystemC)

- experience with verification languages (e, Vera)
- programmed the latest Xilinx and Altera FPGAs
- knowledge of digital logic synthesis, digital layout, static timing analysis and formal verification tools (Design Compiler, Astro, PrimeTime, Formality)
- knowledge of integrated circuit verification techniques
- experience in the programming of DSPs
- knowledge of UNIX/Linux operating systems and the LaTeX document preparation system

Please forward your CV to the address below and we will acknowledge receipt within one working week:

email:	recruitment@xerenet.com	
web:	http://www.xerenet.com	
post:	Xerenet, The Rubicon Centre, Bishopstown, Cork, Ireland	